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## **BIB DATA SHEET**

## **CONFIRMATION NO. 8500**

SERIAL NUMBER	FILING or 371(c)	CLASS	GROUP ART	OUP ART UNIT		ATTORNEY DOCKET	
09/943,196	08/30/2001	438	1792	1792		01-146	
	RULE						
APPLICANTS Charles E. May, Gresham, OR;							
** CONTINUING DATA **********************************							
** FOREIGN APPLICATIONS ************************************							
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/02/2001							
Foreign Priority claimed Yes No 35 USC 119(a-d) conditions met Yes No  Met after Allowance		STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS		INDEPENDENT CLAIMS	
Verified and Acknowledged /MAKI A A Examiner's		OR	2	20		3	
ADDRESS							
Sandeep Jaggi c/o Mark Salvatore LSI Logic Corporation, M/S D-106 1551 McCarthy Boulevard Milpitas, CA 95035 UNITED STATES							
TITLE							
Arrangement and method for fabricating a semiconductor wafer							
RECEIVED No				□ All Fees □ 1.16 Fees (Filing) □ 1.17 Fees (Processing Ext. of time) □ 1.18 Fees (Issue) □ Other □ Credit			